103. (New) The method of claim 102 wherein a processing unit appropriate for the second instruction is a memory unit.

104. (New) The method of claim 102 wherein a processing unit appropriate for the second instruction is an arithmetic logic unit.
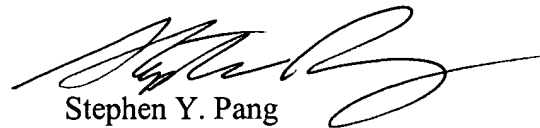
105. (New) The method of claim 101 wherein issuing the first instruction to a processing unit comprises using a switching unit to couple the first instruction to the processing unit appropriate for the first instruction in response to the issue data. --

CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-326-2400.

Respectfully submitted,

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